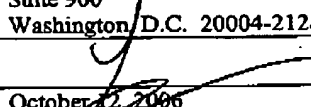
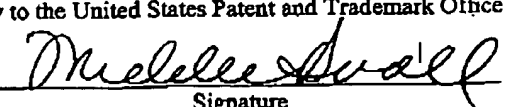


OCT 12 2006

<b>TRANSMITTAL FORM</b> (to be used for all correspondence after initial filing)	Application Number	10/811,835
	Filing Date	March 30, 2004
	First Named Inventor	Kazuaki GOTO
	Group Art Unit	2825
	Examiner Name	Yelena Rossosheek
Total Number of Pages in This Submission	Attorney Docket Number	030712-29

ENCLOSURES (check all that apply)		
<input type="checkbox"/> Fee Transmittal Form <input type="checkbox"/> Fee Attached <input checked="" type="checkbox"/> Amendment / Reply <input type="checkbox"/> After Final <input type="checkbox"/> Affidavits/declaration(s) <input type="checkbox"/> Extension of Time Request <input type="checkbox"/> Express Abandonment Request <input type="checkbox"/> Information Disclosure Statement <input type="checkbox"/> Certified Copy of Priority Document(s) <input type="checkbox"/> Response to Missing Parts/Incomplete Application <input type="checkbox"/> Response to Missing Parts under 37 CFR 1.52 or 1.53	<input type="checkbox"/> Assignment Papers (for an Application) <input type="checkbox"/> Drawing(s) <input type="checkbox"/> Declaration and Power of Attorney <input type="checkbox"/> Licensing-related Papers <input type="checkbox"/> Petition <input type="checkbox"/> Petition to Convert to a Provisional Application <input type="checkbox"/> Power of Attorney, Revocation Change of Correspondence Address <input type="checkbox"/> Terminal Disclaimer <input type="checkbox"/> Request for Refund <input type="checkbox"/> CD, Number of CD(s) _____	<input type="checkbox"/> After Allowance Communication to Group <input type="checkbox"/> Appeal Communication to Board of Appeals and Interferences <input type="checkbox"/> Appeal Communication to Group (Appeal Notice, Brief, Reply Brief) <input type="checkbox"/> Proprietary Information <input type="checkbox"/> Status Letter <input type="checkbox"/> Application Data Sheet <input type="checkbox"/> Request for Corrected Filing Receipt with Enclosures <input type="checkbox"/> A self-addressed prepaid postcard for acknowledging receipt <input type="checkbox"/> Other Enclosure(s) (please identify below):
Remarks		<input checked="" type="checkbox"/> The Commissioner is hereby authorized to charge any additional fees required or credit any overpayments to Deposit Account No. 19-2380 for the above identified docket number.

SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT	
Firm or Individual name	Luan C. Do, Reg. No. 38,434 Nixon Peabody LLP 401 9 <sup>th</sup> Street, N.W. Suite 900 Washington, D.C. 20004-2128
Signature	
Date	October 12, 2006

CERTIFICATE OF MAILING OR TRANSMISSION [37 CFR 1.8(a)]	
I hereby certify that this correspondence is being:	
<input type="checkbox"/> deposited with the United States Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to: Mail Stop _____, Commissioner for Patents, P. O. Box 1450, Alexandria, VA 22313-1450	
<input checked="" type="checkbox"/> transmitted by facsimile on the date shown below to the United States Patent and Trademark Office at (571) 273-8300.	
October 12, 2006 Date	 Signature Michelle Duvall Typed or printed name

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Docket No. 030712-29  
Application No. 10/811,835  
Page 1

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Kazuaki GOTO

Application No.: 10/811,835

Filed: March 30, 2004

For: METHOD OF DESIGNING A CIRCUIT  
OF A SEMICONDUCTOR DEVICE

Confirmation No.: 3051

Art Unit: 2825

Examiner: Rossosheek, Yelena

Date: October 12, 2006

I hereby certify that this correspondence is being facsimile transmitted to the USPTO at 571-273-8300 on October 12, 2006.

  
Michelle DuvallAMENDMENTMail Stop AMENDMENT  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, Virginia 22313-1450

Sir:

In response to the Examiner's Office Action mailed April 12, 2006, please consider the following amendments and remarks in connection with the above-identified application.

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Docket No. 030712-29  
Application No. 10/811,835  
Page 3

OCT 12 2006

**IN THE TITLE:**

Please replace the title of the invention with the following new title of the invention:

METHOD OF DESIGNING A CIRCUIT OF A SEMICONDUCTOR DEVICE  
WITH A SPARE UNDERGROUND CELL.

10154240.2